TRA	NSMITT	AL OF INFORMA (Under 37 CFR	Docket No. 200-0665										
In Re Application of: Joseph G-Walacavage et al.  JUN 0 2 2006													
Applic	cation No.	Filing Date	Examiner	Customer No.									
	965,904	September 28, 2001	J. Proctor	33481	2123	4251							
Title: METHOD OF LOGICAL MODELING OF OPERATOR INTERACTION WITH PROGRAMMABLE LOGIC CONTROLLER LOGICAL VERIFICATION SYSTEM													
Address to:  Commissioner for Patents P.O. Box 1450  Alexandria, VA 22313-1450													
			37 CFR 1.97(b)										
i. <u>ta</u>	1. Image: The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.												
2.	2. The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:												
	☐ the	statement specified in	n 37 CFR 1.97(e);										
		(	OR										
	☐ the	fee set forth in 37 CF	FR 1.17(p).										

			0		ATTY DOCKET N		APPLICATION NO.								
	INFORMATION DISCLOSURE CITATION					200-0665 APPLICANT(S)		09/965,904							
(Use several sheets if necessary)    Joseph G. Walacavage et al.     Filing Date   GROUP ART UNIT     September 28, 2001   2123															
		(000 0000 0000 0000 0000 0000 0000 0000 0000	一"信 ~ ~ ?	7006 PP87	FILING DATE	acavage or an	GROUP ART UNIT								
(Use several sheets if necessary)			The second	1006		er 28, 2001	2123								
U.S. PATENT DOCUMENTS															
*EXAMINER		DOCUMENT NUMBER	DATE		NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE							
111111111111111111111111111111111111111															
									···						
-	. U.S. PATENT APPLICATION PUBLICATIONS														
*EXAMINER		DOCUMENT NUMBER	DATE		NAME	CLASS	SUBCLASS	FILING DAT							
IŅĪTIAL															
			FOREI	GN PATE	NT DOCUMENTS	<u> </u>		<u> </u>							
						GUIDOL 400	TRANSLATION								
		DOCUMENT NUMBER	DATE		COUNTRY	CLASS	SUBCLASS	YES	NO						
-		OTHER DOCUMEN	NTS (Including	g Author,	Title, Date, Pe	ertinent Pages, Et	c.)								
		S. Kanai, T. Kishinami,	"A Virtual Verti	fication <b>F</b>	Environment for t	he Sequence Contro	l System Using	VRML and	Ŀ						
	S. Kanai, T. Kishinami, "A Virtual Vertification Environment for the Sequence Control System Using VRML and JAVA," 1999 by ASME, pages 1-8.  "ThermaView Advanced Welder Diagnostics System," 1998.														
EXAMINE	R				DATE CONSIDER	ED									
*EXAMINE	*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not														
considered	. includ	de copy of this form with next comm	nunication to applica	ant.											

Form PTO-A820 (also form PTO-1449) P09C/REV04

Patent and Trademark Office \* U.S. DEPARTMENT OF COMMERCE